



Patent Application No. 10/801,234  
Customer Number: 42717

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Fu-Hsin Chen, et al.	§	Docket No.:	24061.79
		§		(TSMC2003-0322)
Serial No.:	10/801,234	§		
		§	Examiner:	Thomas L. Dickey
Filing Date:	March 16, 2004	§		
		§	Art Unit:	2826
For:	High-Voltage MOS Transistor	§		
	and Method for Fabricating	§	Conf. No.:	2028
	the Same	§		
		§		

**RESPONSE TO RESTRICTION REQUIREMENT AND AMENDMENT**

Mail Stop: Amendment  
Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

The present paper is being submitted in response to the Restriction Requirement Office Action dated March 18, 2005 in the above-identified application.

**Election and Amendment to the Claims** are reflected in the listing the claims which begin on page 2 of this paper.

**Remarks** begin on page 7 of this paper.